

Appln.:

Christl Lauterbach, et al.

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For:

ELECTRONIC CIRCUIT FOR A METHOD FOR

INFORMATION WITH FERROELECTRIC FLIP-FLOPS

Confirmation No.:

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Attorney:

Jeffrey R. Stone

Attorney Docket No.: 32226.17

Additional Fees:

Charge to Deposit Account 023732

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

TRANSMITTAL COVER LETTER

Enclosed for filing, please find the following:

1. Amendment and Response (8 pgs.);

- Petition for Extension of Time Under 37 CFR 1.136(a); 2.
- A check in the amount of \$950.00; and 3.

٠,

Postcard receipt.

Respectfully submitted

Jeffrey K. Stone (Reg. No. 47,976)

BRIGGS AND MORGAN

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CERTIFICATE OF EXPRESS MAILING

I hereby certify that this document, along with the documents referenced above, are being deposited with the United States Postal Service as Express Mail, Receipt No. EV 190456540 US in a envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

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28. (new): The electronic circuit as claimed in claim 1, wherein the control means can generate at least one switch-off control signal after a predetermined time has elapsed after the at least one information signal arise and when the energy converted from the at least one information signal is exhausted,

wherein the signal processing means can be caused or is caused to effect a storage and to effect deactivation by the at least one switch-off control signal.

- (new): The electronic circuit as claimed in claim 1 or 2, wherein the information stored in the at least one ferroelectric flip-flop can be converted into at least one output signal by the signal processing means and the electronic circuit furthermore has at least one output for outputting the at least one output signal.
- 30. (new): The electronic circuit as claimed in claim 1, wherein the electronic circuit furthermore has a display means for displaying the information stored in the at least one ferroelectric flip-flop.
- 31. (new): The electronic circuit as claimed in claim 4, wherein the display means is concomitantly supplied by the voltage supply generated by the energy means.
- 32. (new): The electronic circuit as claimed in claim 4 or 5, wherein the display means has an LCD display.
- 33. (new): The electronic circuit as claimed in claim 3, wherein an external voltage supply and external control means can be connected for the outputting of the information stored in the at least one ferroelectric flip-flop by the signal processing means.
- 34. (new): The electronic circuit as claimed in claim 2, wherein the at least one switch-off control signal has the following signals:

a transfer end signal;

an activation signal for activating precharge transistors of at least one ferroelectric flip-flop; and

a current switch-off signal for switching off the voltage supply of the signal processing means.

35. (new): The electronic circuit as claimed in claim 1, wherein signal lines for each of the switch-on signals lead from the control means to the signal processing means.